



Category: MOSFET

**CIRCUIT IDEAS FOR DESIGNERS**

Schematic no. fet\_11113.0

**Differential EPAD® MOSFET Matched-Pair Circuit with Buffered and Summed Outputs****Description**

The matched pair EPAD MOSFET Array provides two separate, matched outputs at DN1 and DN2. Each output could be biased to provide the same type of output characteristics. For example, both outputs can be designed to provide the same matched, temperature coefficient. Or, one output DN1 can be biased to give a different temperature coefficient from DN2. In this circuit, the two outputs DN1 and DN2 has been buffered by an operational amplifier. The two outputs are then summed through a summing amplifier to give a single output  $V_o$ . By careful design of bias voltages and selection of components, this circuit can yield a wide range of desired temperature characteristics of its output,  $V_o$ .

For full schematic diagram and notes, please register and login at [aldinc.com](http://aldinc.com)