**Current Source Multiplication**

![Circuit Diagram](image)

**Description**

This is a current multiplication circuit. Current $I_{SET}$ is set by determining $V+$ and $R_{SET}$ values. $I_{SET}$ is given by the equation: $I_{SET} = (V+ - V_{DS}) / R_{SET}$ where $V_{DS}$ is the drain and gate connected voltage of $Q_{SET}$ at the selected current level $I_{SET}$. The output current $I_{SOURCE}$ is given by $I_{SOURCE} = (n \times I_{SET})$ where $n$ is the number of FETs connected in parallel. Note that all $Q$, $Q_{SET}$, $Q_1$, $Q_2$ .. $Q_N$ must be of the same device type and the same threshold voltages but not necessarily from the same IC package. Devices from the same package would deliver better results due to close tracking of adjacent MOSFETs. ALD1108xx devices would generally give better results. If $Q_{SET}$ is from ALD1108xx, then $Q_1$ through $Q_N$ devices would generally give better results. If $Q_{SET}$ is from ALD1108xx, then $Q_1$ through $Q_N$ must also be from the same ALD1108xx types.

**Recommended Components**

ALD1101, $\frac{1}{2}$ ALD1103, ALD1106, ALD1116, ALD1108xx, ALD1109xx

**Other Related Circuit Ideas**

Schematic no. cs_11007.0 High Output Impedance Current Source