High Output Impedance Cascode Current Mirror

Description

This current source uses a voltage reference and a precision reference resistor that produces an output current that has substantially higher dynamic output impedance, particularly for low current levels of less than 100μA. This current source buffers the reference current and produces a constant current for a wide voltage range. This current source utilizes two pairs of N-channel matched pair MOSFETs connected in a cascode configuration. The bottom MOSFET pair must be a matched pair and the top MOSFET pair must also be a matched pair. However, the top pair and the bottom pair do not have to be the same model. The lower voltage compliance range is determined by the $2 \times V_{gs(th)}$ and the upper voltage compliance voltage are limited by breakdown voltage. Use very low $V_{gs(th)}$ devices such as ALD110802 and ALD110800 to reduce the lower voltage compliance limit.

For full schematic diagram and notes, please register and login at aldinc.com